PALLAVI SHARMA

D/O Tirath Ram Sharma Dashmesh Nagar, H.No1, Dagana Road, Hoshiarpur.

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OBJECTIVE:

To excel in the software/hardware profession and hold up a challenging position in corporate world through diligence and dedication and to ensure my highest contribution towards the organization I work with.

EDUCATIONAL QUALIFICATIONS:

Examination	School/College	Board/ University	Year of Passing	%age
Ph.D. (ECE)	SBBSU	SBBSU	2015	Pursuing
M.Tech.(ECE) Part Time	GNDEC, Ludhiana	Punjab Technical University	2013	65.36%
B.Tech.(ECE)	SBBSIET College, Padhiana	Punjab Technical University	2010	80.57%
XII	S.D Senior Sec. School, Hoshiarpur	P.S.E.B (Mohali)	2006	62.66%
Matriculation	D.A.V. Public School, Garhdiwala	P.S.E.B (Mohali)	2004	77.64%

EXPERIENCE:

- ❖ Working as an **Assistant Professor** at Lyallpur Khalsa College of Engineering, Jalandhar since 5th Jan 2015.
- ❖ Worked as an **Assistant Professor** at Rayat Bahra Institute of Engineering & Technology, Hoshiarpur from 9th Jan 2012- 15th Dec 2014.
- ❖ Worked as a Lecturer at YRS Polytechnic College, Moga from 1st Feb 2011- 7th Jan 2012.

PUBLICATIONS:

International Conference:

- ➤ "A Hybrid Approach Based Broadcast Algorithm in Wireless Adhoc Networks" International Conference ICIEEE, 2013 at SARC, Pune.
- "Simulation of Planar Inverted F Antenna for On-Body and Free Space Communication" International Journal of Recent and Innovation Trends in Computing and Communication, 2016.

➤ "Efficient Local Broadcast Algorithms in Wireless Ad Hoc Networks using Hybrid Approach" International Journal of Innovative Research in Computer and Communication Engineering, 2016.

National Conference:

- ➤ "Virtual Reality Ambiance & Atmosphere", National Conference of Recent Advances in Communication & Electronics at Rayat Bahra Group of Institutes, Hoshiarpur, 2012.
- ➤ "Nanoantennas", National Conference of Recent Advances in Communication & Electronics at Rayat Bahra Group of Institutes, Hoshiarpur, 2012.

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PROJECTS:

- Robotic based PIR sensor.
- ➤ Unregulated Power Supply.
- Application of op-amp log and antilog amplifier.

TRAINING:

- ➤ 6 weeks training of MCSE from SBBSIET.
- ➤ 6 weeks training of Embedded system from Techno School, Chandigarh.
- ➤ 6 months training in BSNL and in .net from CS INFOTECH, Chandigarh.

Area of Interest:-

- Digital Electronics.
- > Microprocessors and Microcontrollers.
- ➤ Digital Signal Processing.
- > Signal and Systems.

SKILLS SUMMARY:

Software Proficiency : C, C++
Hardware Proficiency : MCSE
Assembly Language programming : 8085, 8051.

STRENGTHS:

- > Self motivating long term player and have precise and clear communication skills.
- > Creative, curious, Consistent and committed to work.
- Ability to successfully handle and deliver any task that is assigned to me despite the time constraints or resource crunch.

DECLARATION:

I commit that all the information given above is true & reliable according to my information.

Date: Sigr
